# M3096V

6/12-LED LOTTO GAME IC

### Description

M3096V is a low voltage 6/12-LED controller IC • CMOS technology specially design for lotto game.

The IC requires minimal external components. • Maximum 12 LED can be connected as :

### Application Example

seconds

- 12 lamps lotto game
- 6 lamps dice game
- 10 lamps bingo game

LED.

- 5 lamps bingo game
- 2 lamps big or small game
- 10+2 lamps big or small game
- Other lotto games

# ■ Product type

Part number	Package type
M3096V	Chip form
3906P	16 pin DIP

Explanation (reference to P.3 application circuits)

- 1. 12 or 6 LED + 1 LED (center LCN LED, auto chasing)
  - (1) Holding SW, 12 LED start to flash sequentially. LCN LED flashes indepently.
  - (2) After released SW, 10 and LCN LED start to slow down flashing speed
  - (3) When it stops at one LED, LCN LED keeps flashing.
  - (4) After 10 seconds, all LED turn off. IC enters standby mode. Retriggering SW will re-start the above sequence.
  - (5) IC can be connected as 12 or 6 LED application.

2. 10 or 5 LED + 2 LED (two center LCN LED, for big or small)

- (1) Holding SW1,10 LED start to flash sequentially. Two LCN LED do not flash.
- (2) After released SW1, 10 LED start to slow down flashing speed.
- (3) When it stops at one LED. Press SW2, LCN(11<sup>st</sup> and 12<sup>nd</sup>) LED will flash and stop at one.
- (4) IC can be connected as 5, 10, 2 LED.
- (5) Using only 11<sup>st</sup> and 12<sup>nd</sup> LED is for 2-LED application.

# Feature

- Operating voltage : 1.35~5.00V DC
- Low standby current  $< 5\mu A$  (@3VDC)
- - 12 or 6 LED, LCN as center LED, IC will automatically stop after 6
  - 10 or 5 or 2 LED, use power on/off LED will chase sequentially for a few cycles and randomly stop at one
- IC directly drives piezo buzzer and generates sound effect which changes with LED chasing speed
- Single trigger input (High Level)

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■ 16 pin DIP package

LP 🗖 2

RP 🗖 3

TRIG 4 LCN 5 K2/L6 6 L5 7

L4 🗖 8

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16 🗖 K1

15 🗖 5/6

14 🗖 BZ2

14 BZ2 13 BZ1 12 VSS 11 L1 10 L2

9 🗖 L3

## ■ IC bonding pad diagram

17 16 15 14 13	12
KP LP VDD TS2 TS1	K1
<b>M3096V</b> 1870 x 1880 μm	5/6 11 BZ2 10 BZ1 9
LCN K2/L6 L5 L4 L3 L2	L1 VSS
1 2 3 4 5 6	7 8
IC substrate Vdd	

### Pin description

Pad	DIP	Symbol	Function
1	5	LCN	Center LED output
2 3 4 5 6 7	6 7 8 9 10 11	K2/L6 L5 L4 L3 L2 L1	SW2 key input or LED6/LED12 output LED5/LED11 output LED4/LED10 output LED3/LED9 output LED2/LED8 output LED1/LED7 output
8	12	Vss	Negative power supply
9 10	13 14	BZ1 BZ2	Buzzer output port 1 Buzzer output port 2
11	15	5/6	5(10)/6(12) LED output selection pin
12	16	K1	SW1 key input
13 14		TEST1 TEST2	Test pin (internal use) Test pin (internal use)
15	1	Vdd	Positive power supply
16 17	2 3	LP RP	Left output Right output

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# ■ Application Circuit

A. Auto power off (lotto game)



- B. Power will not turn off automatically (bingo game)
  - (1) 5 LED + 2 LED

(2) 10 LED + 2 LED

